

# SMIC MPW Shuttle Schedule

**MPW Notice** To serve all customers smoothly, following items must be noticed:

1. Only standard Process/Layers products can attend MPW. No bumping, No Bank and No Corner Split allowed.
  2. On one shuttle, 4 seats (including sub-chip in one seat) are the maximum that one customer can get.
  3. **MPW only provides 50 dies for function verification.**
  4. Shuttles are subject to cancellation if there are not enough passengers on board.
  5. **Without completion of below items before shuttle start date, shuttle reservation will not be held!**
- \*Quotation should be ready/ DRC must be clean/ SMIC IP merge case must be closed (related information need to be submitted at least 3 days before shuttle start date)
- \*GDSII and tape out forms all need to be Approved by mask shop/ PTOS should be Approved
6. SMIC dicing size limit: 1500um < X < 12000um, 1500um < Y < 12000um.
  7. For 300mm shuttle, suggest use metal scheme condition: 6(M1-M6)+TM1(9kA)+TM2(9kA)+14.5kA ALPA+12mil BG.
  8. Overdue MPW booked cases will be cancelled within 90 days after shuttle start.

Tech Node	IO Voltage/Tech Type/Char	CMOS RF	2022 MPW Booking Cut-Off Date												
			Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec	
28nm	IO=1.8V   IO=2.5V CMOS Logic (HP)	Y		8 Q9E (Fab2)				10 Q9R (Fab2)			2 QAD (Fab2)			1 QAE (Fab2)	
40nm	IO=2.5V CMOS Logic (LL UP)	Y		8 Q9F (Fab2)			5 Q9P (Fab2)			5 Q9X (Fab2)			11 QA6 (Fab2)		6 QAF (Fab2)
	IO=8/32V CMOS High Voltage (HV)		4 Q9B (Fab2)							12 Q9Y (Fab2)					
	IO=1.8V   IO=2.5V Adv. Emb-Flash (Cu-BEOL with AL-TM2 + RDL process) (LL)							17 Q9S (Fab2)						8 QAB (Fab2)	
55nm	IO=1.8/2.5V   IO=1.8V   IO=2.5V CMOS Logic (LL)	Y							7 Q9U (Fab2)						13 F QAG (Fab2)
	IO=6/32V   IO=8/32V CMOS High Voltage (HV) Do not support 2XTM(STM)		4 Q9C (Fab2)		15 Q9J (Fab2)					26 Q9Z (Fab2)			18 QA8 (Fab2)		
90nm	IO=5V BCD with performance enhanced (EP)							10 QAK (Fab 2)						15 QAL (Fab 2)	
0.11um	IO=3.3V Adv. Emb-Super flash (AL-BEOL) (LL)				8 Q9M (Fab1)								11 QA7 (Fab1)		
0.13um	IO=3.3/5V   IO=5V Adv. Emb-EEPROM (Cu-BEOL) (LL)				22 Q9N (Fab1)						9 QA1 (Fab1)				
0.11/0.13um	IO=3.3V CMOS Logic (GE)   Mixed Signal (GE)	Y		22 Q9G (Fab1)				17 Q9T (Fab1)			16 QA2 (Fab1)				20 QAH (Fab1)
0.18um	IO=5 BCD V3E (EP)		4 Q9D (Fab7)		8 Q9L (Fab1)				21 Q9W (Fab7)			6 QA4 (Fab1)		22 QAC (Fab7)	
0.18um	IO=3.3V CMOS Logic (GE)   Mixed Signal (GE)	Y		15 Q9H (Fab1)			12 Q9Q (Fab1)			7 Q9V (Fab7)	26 QA0 (Fab1)		6 QA5 (Fab7)	25 QA9 (Fab1)	27 QAJ (Fab7)
	IO=5/10/12/20/35/40V BCDM (BCDM do not support RF)														
	IO=3.3/5V   IO=5V EEPROM Embedded (GE)										23 C QA3 (Fab1)				